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21039

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10051726	FILING DATE 01/17/2002	CLASS 327	SUBCLASS 87	GAU 2836	EXAMINER FLEMING Rios
**APPLICANTS: Uy Allan Paul; McConnell Richard;					
**CONTINUING DATA VERIFIED: THIS APPLN CLAIMS BENEFIT OF 60/264,533 01/26/2001					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials				SIRF.133USU1	
TITLE : Method and apparatus for selectively maintaining circuit power when higher voltages are present					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-21)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING
Amount Due	Date Paid		Sheets Drwg. Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE Application Examiner	
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